

FIG. 2

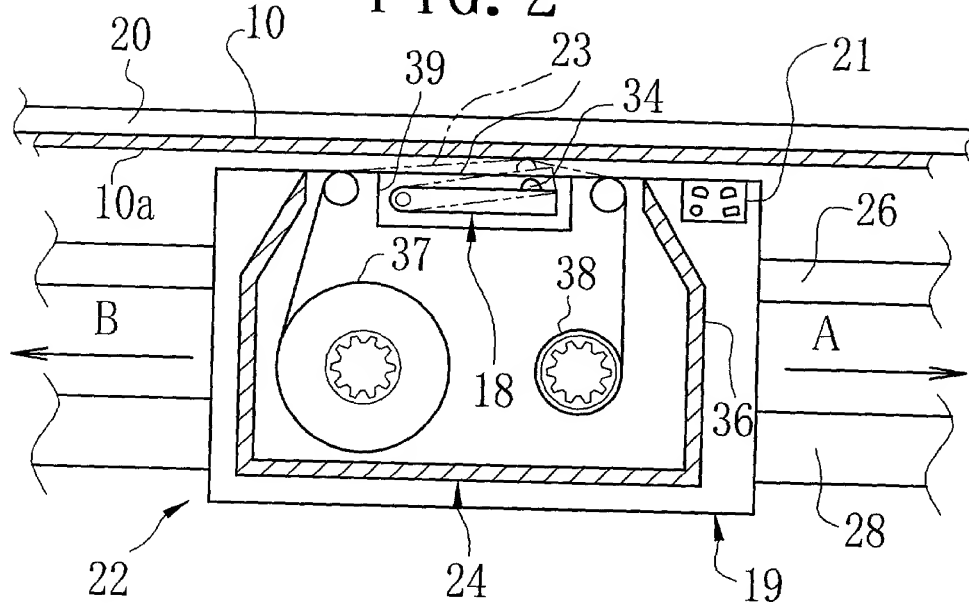


FIG. 3

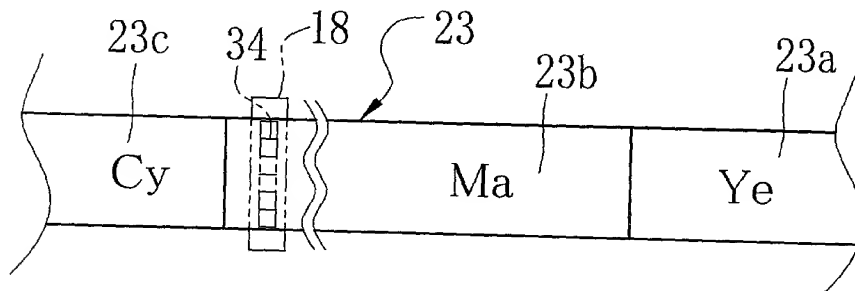


FIG. 4

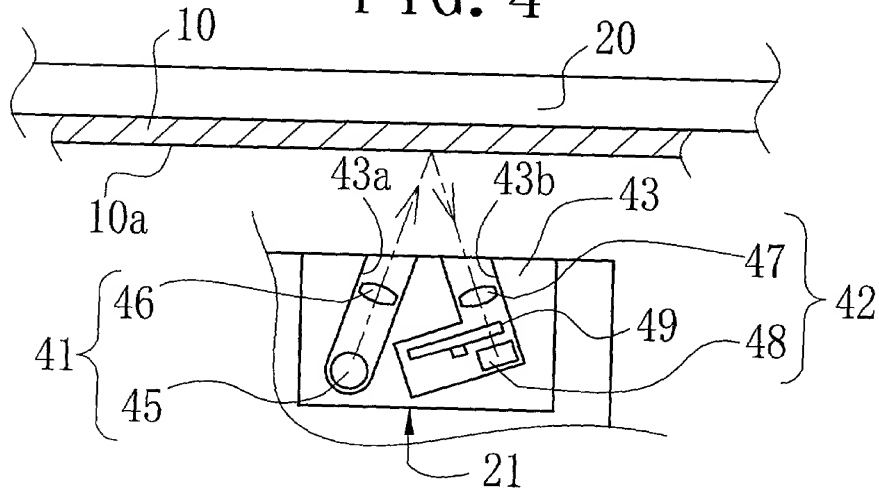


FIG. 8

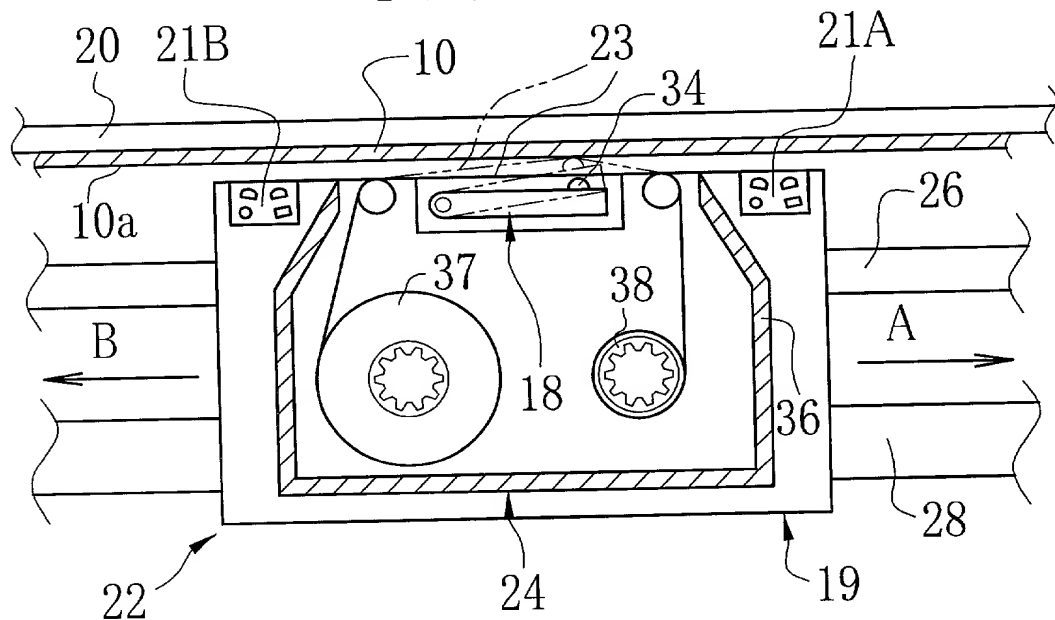


FIG. 6

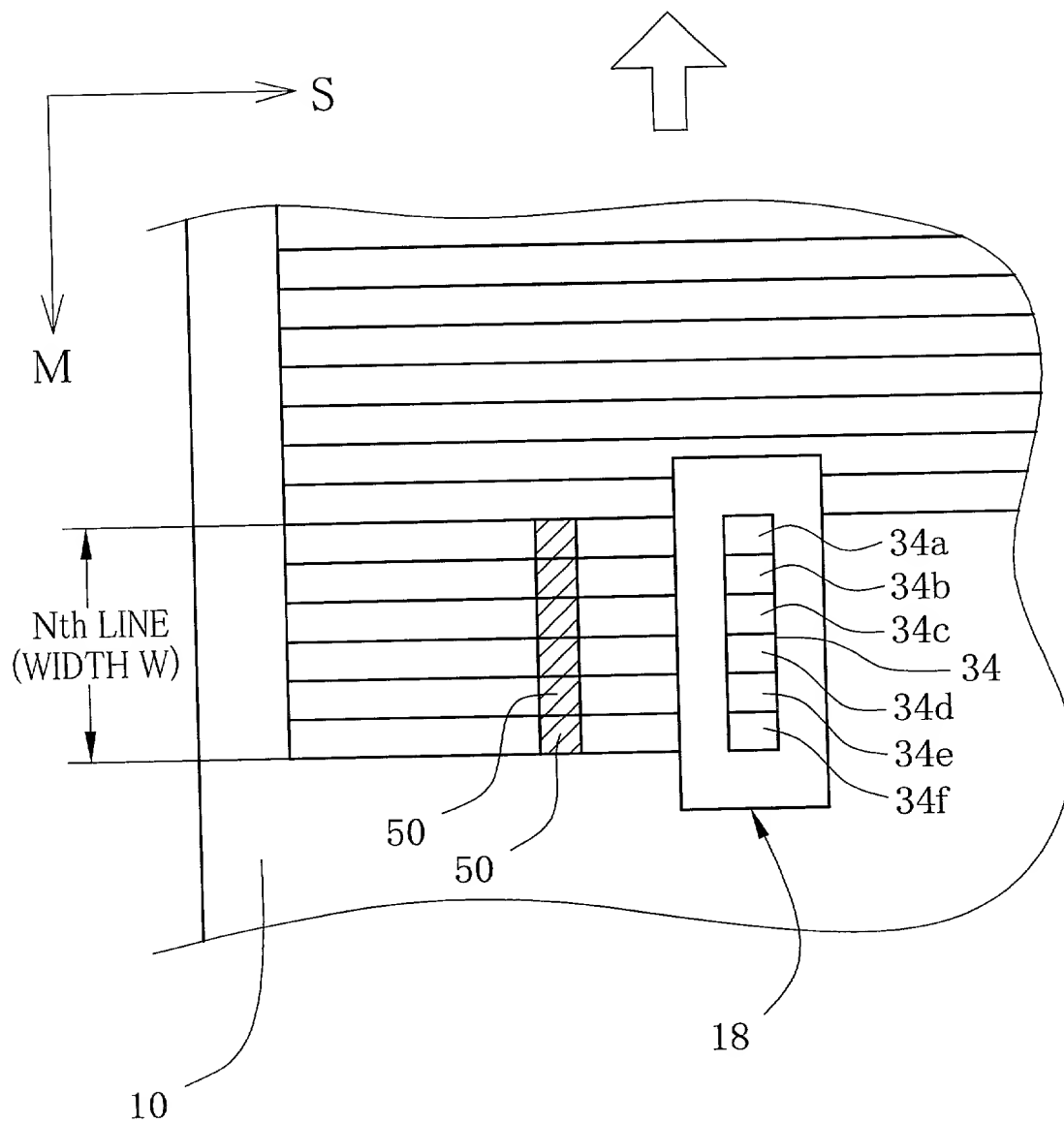


FIG. 7

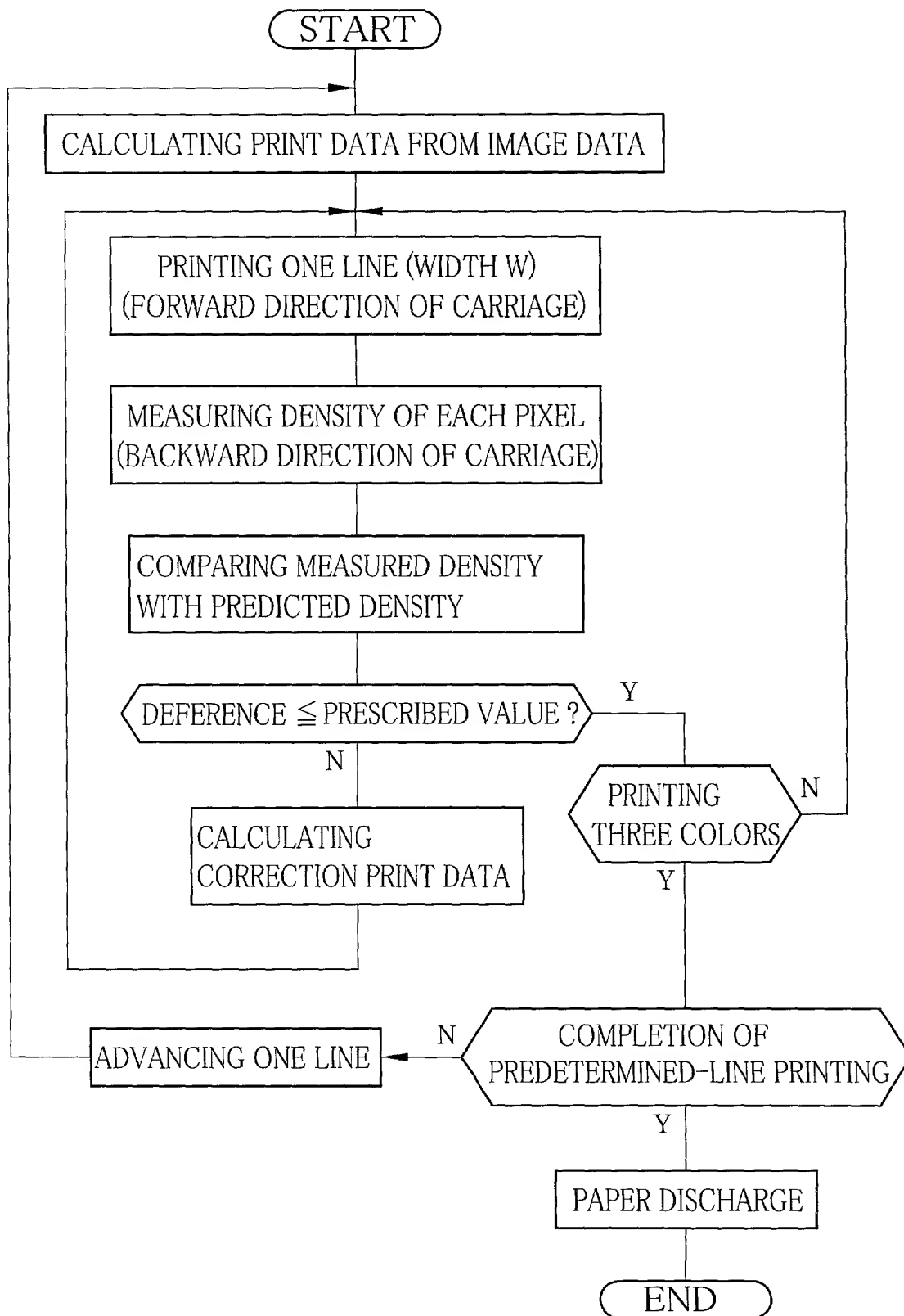


FIG. 9

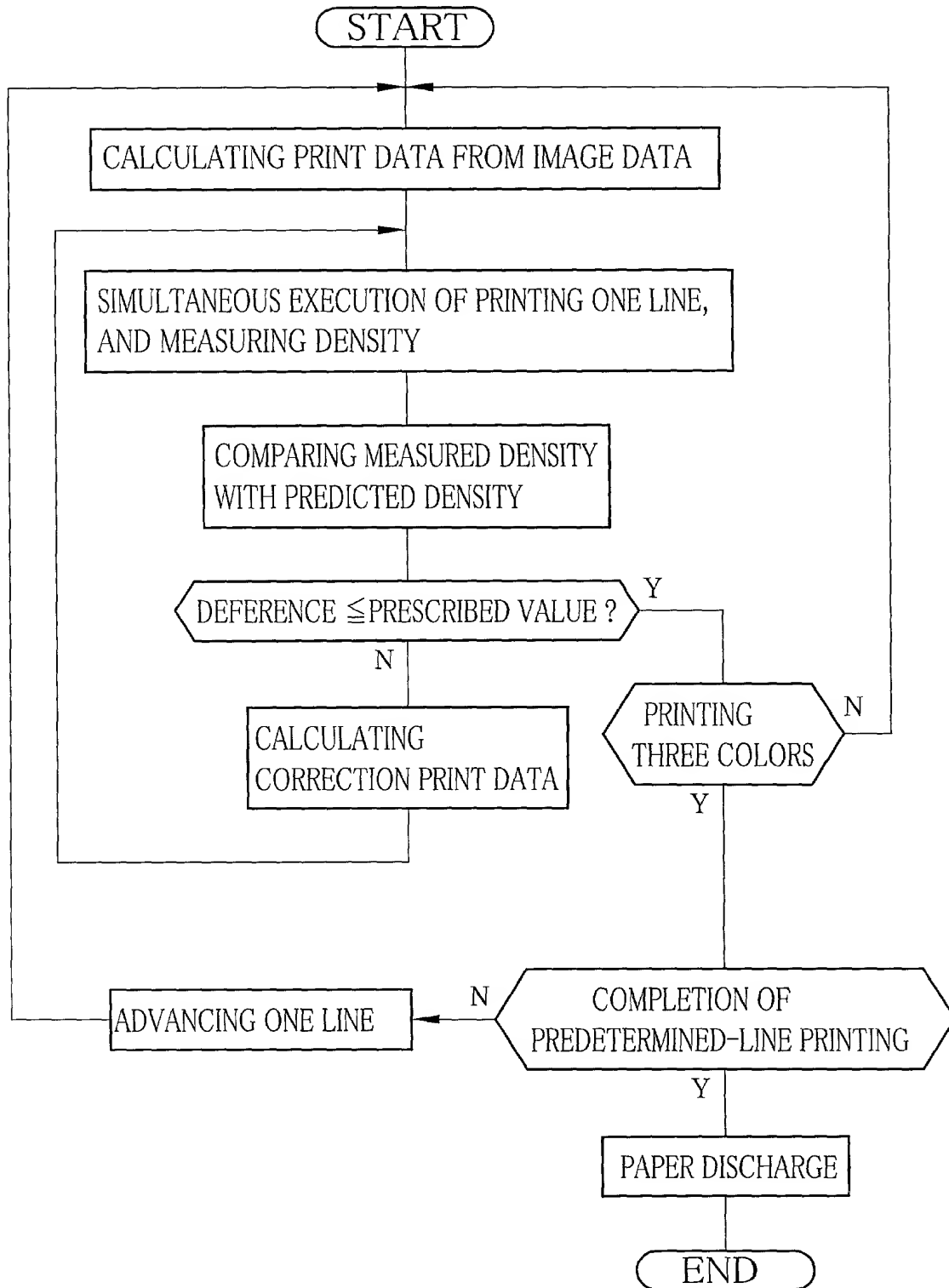
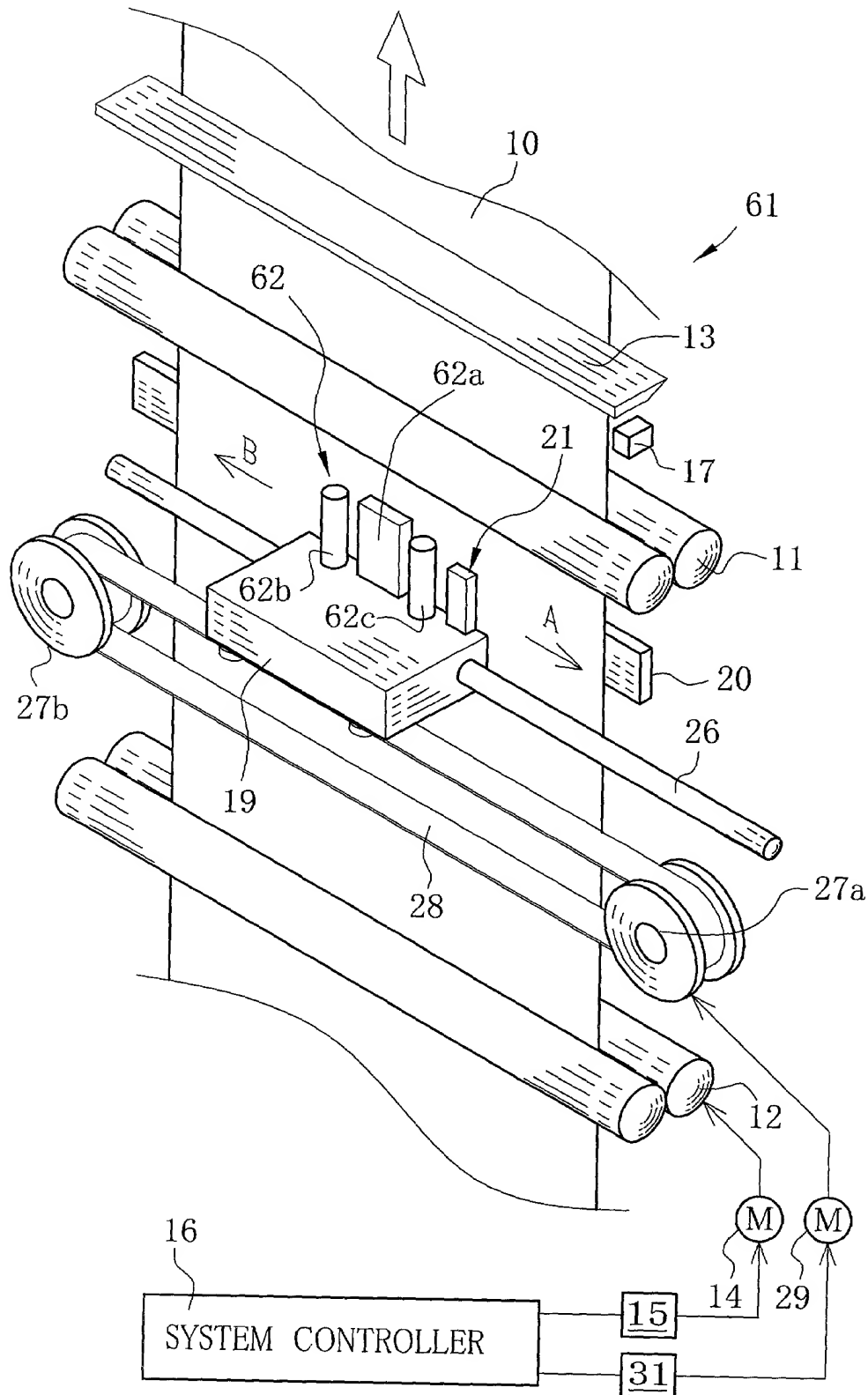


FIG. 10



SYSTEM CONTROLLER

FIG. 12A

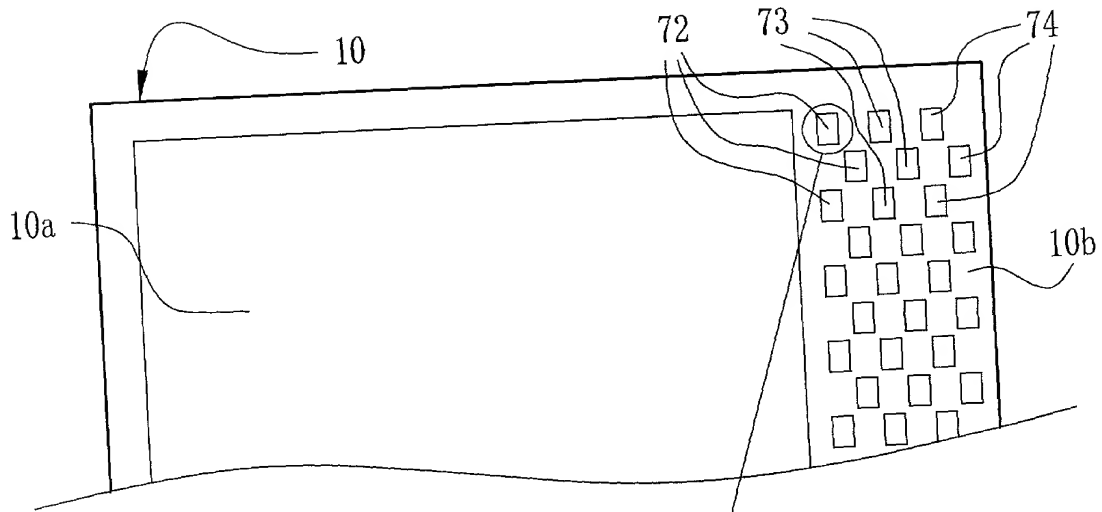


FIG. 12B

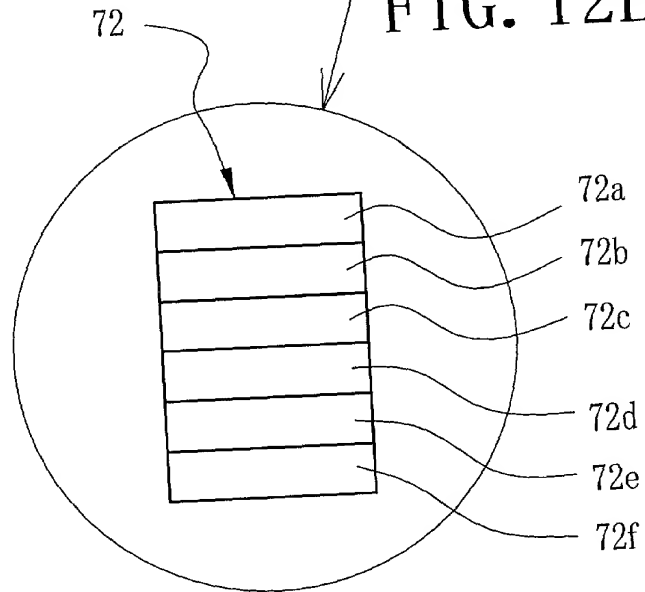


FIG. 13

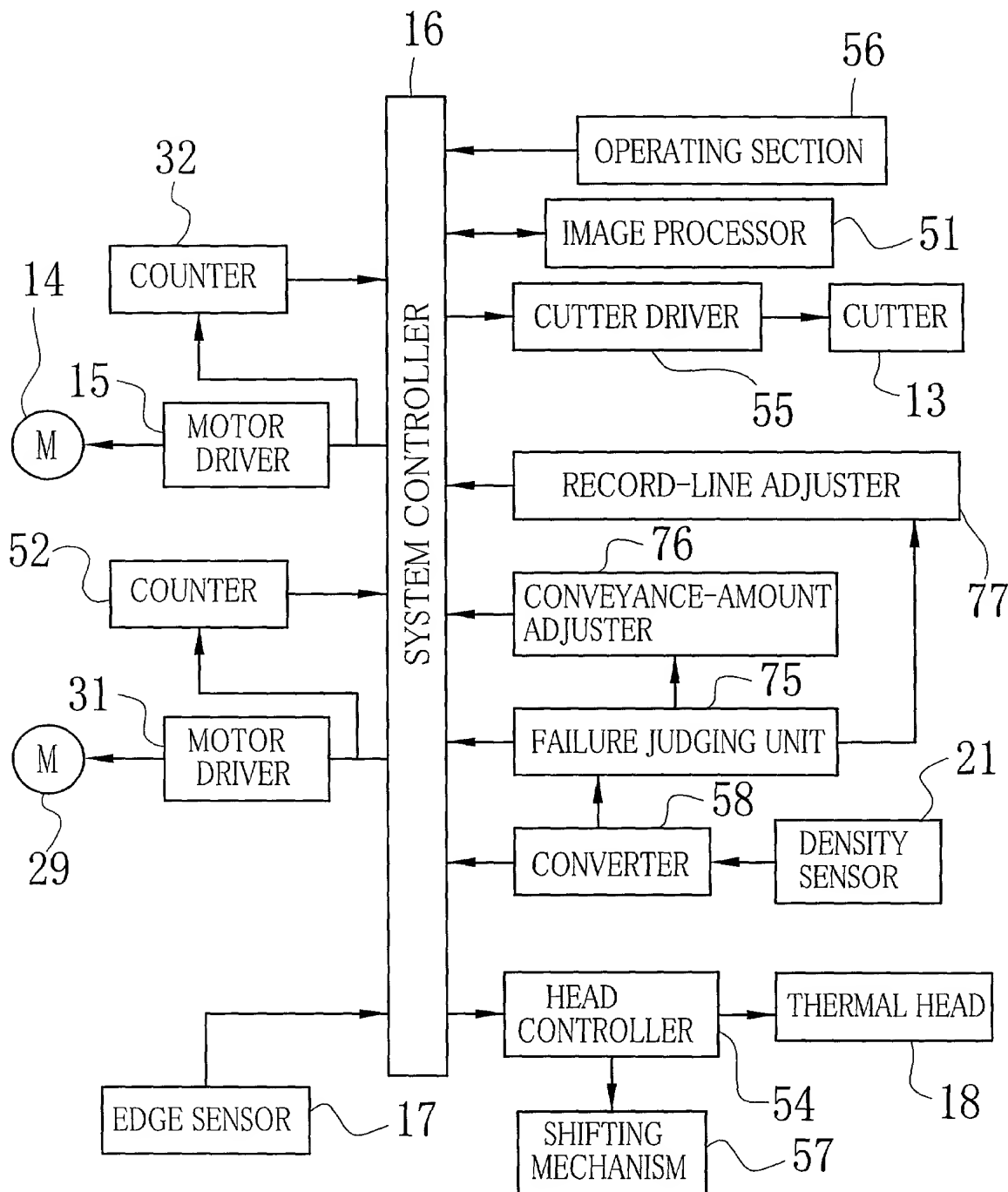


FIG. 14

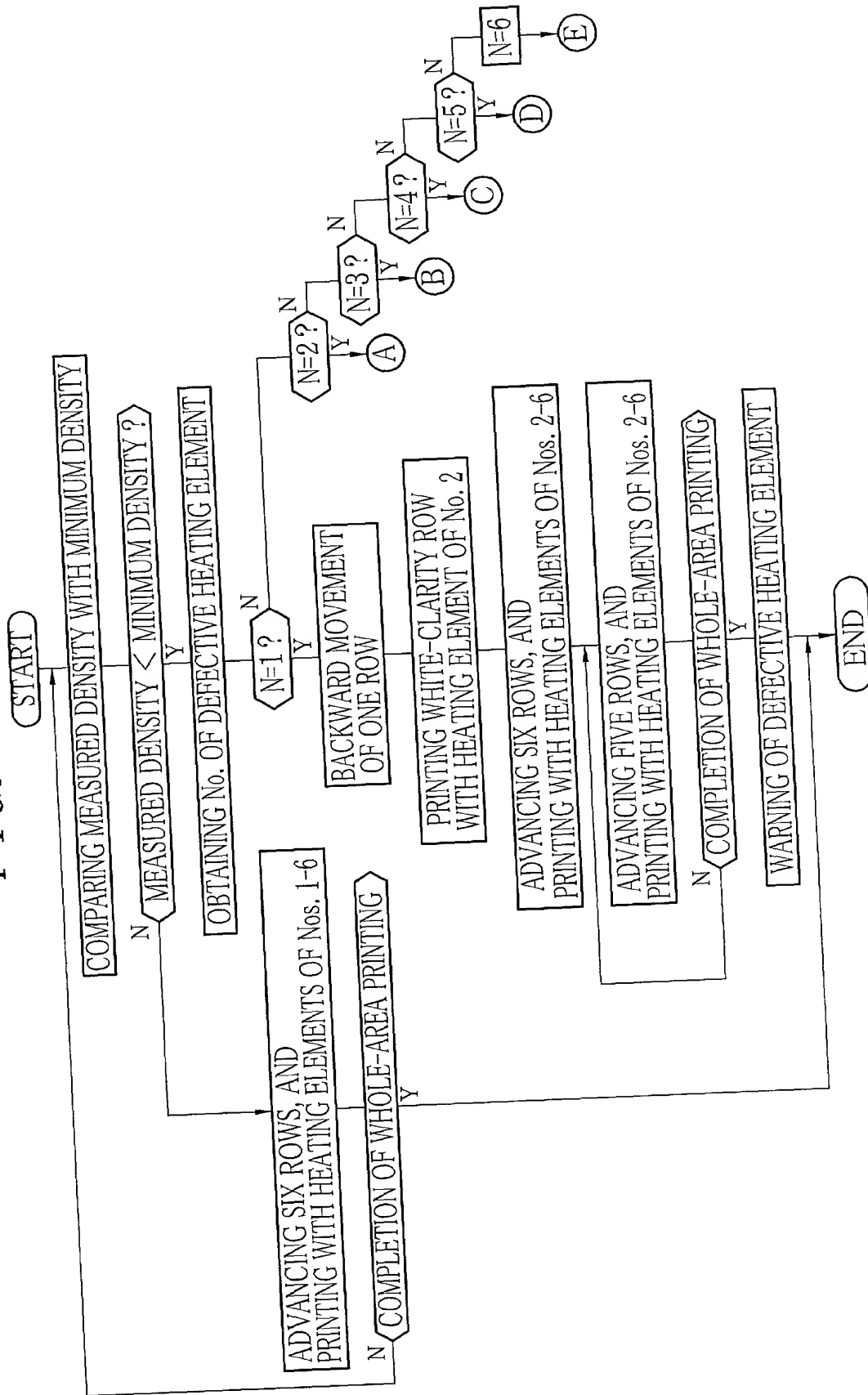


FIG. 15

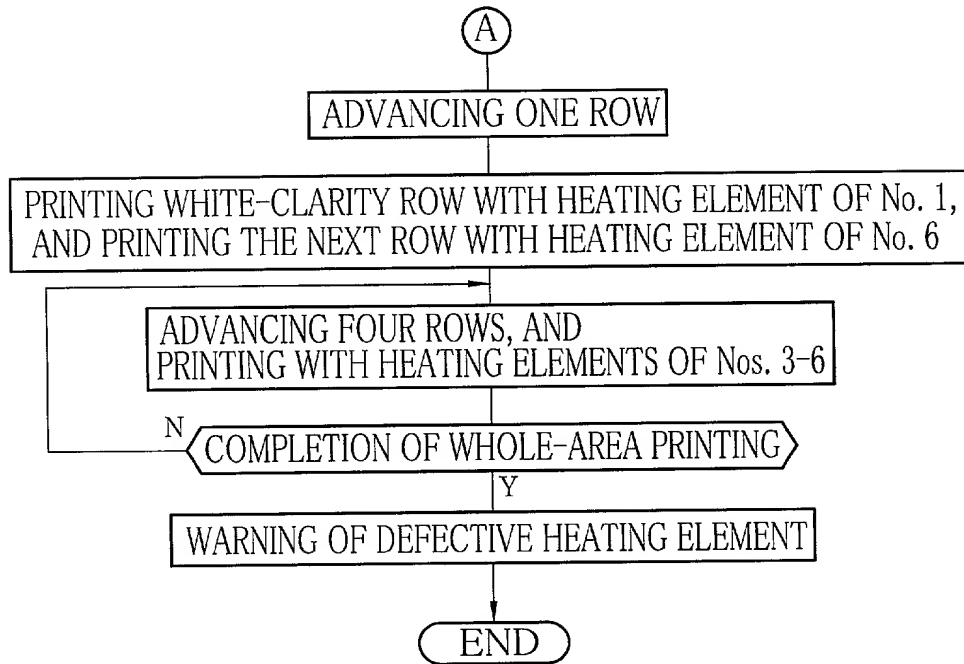


FIG. 16

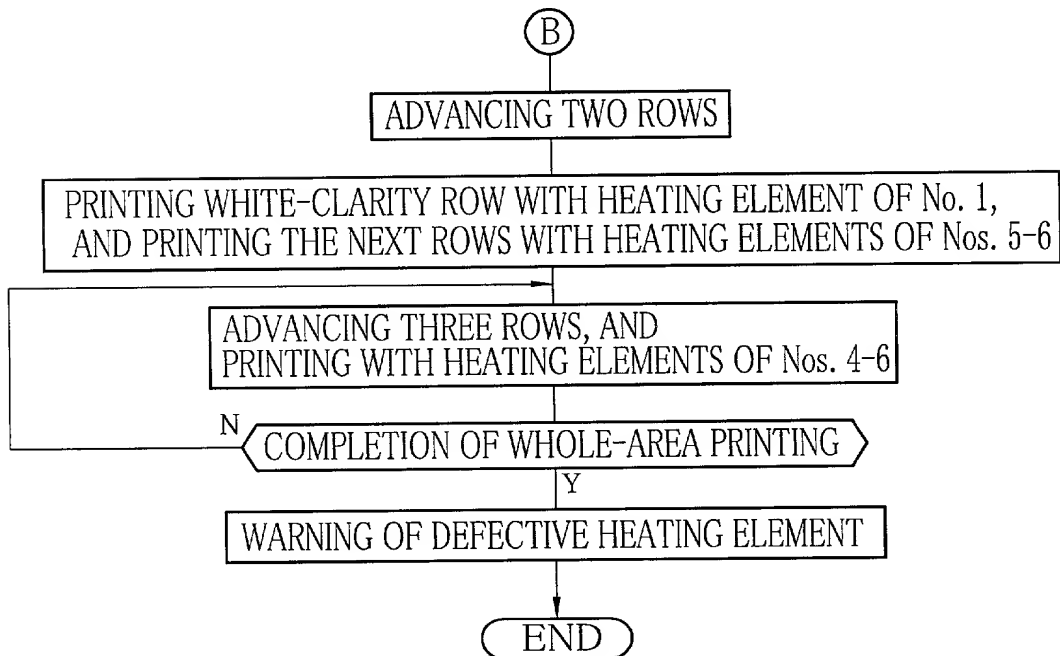


FIG. 17

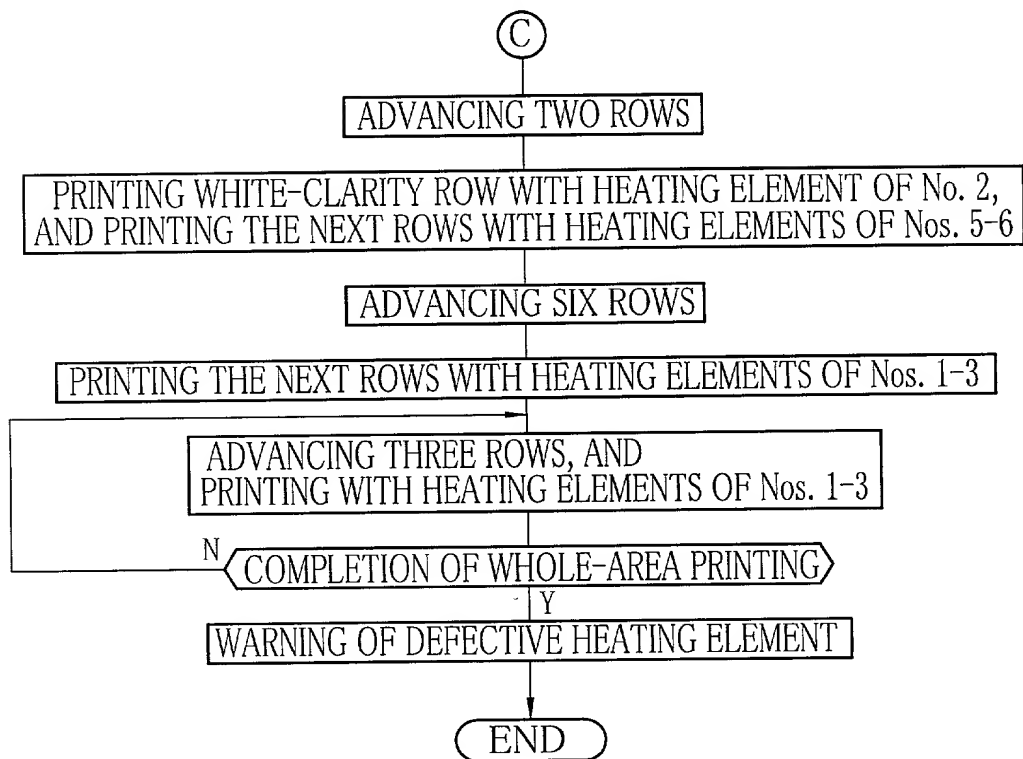


FIG. 18

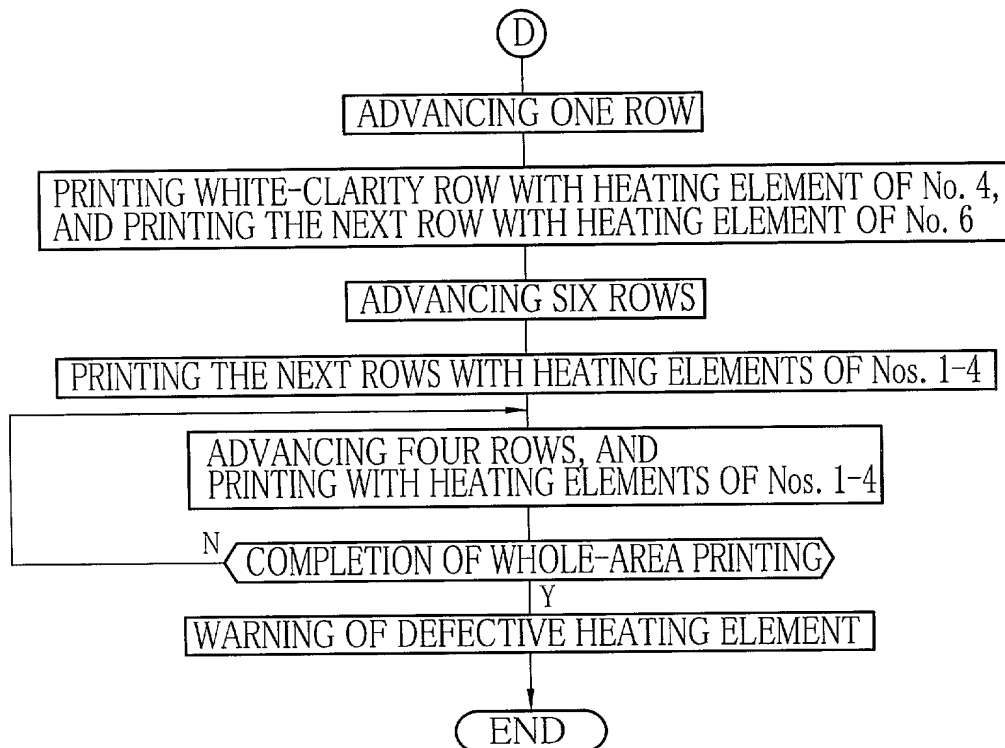


FIG. 19

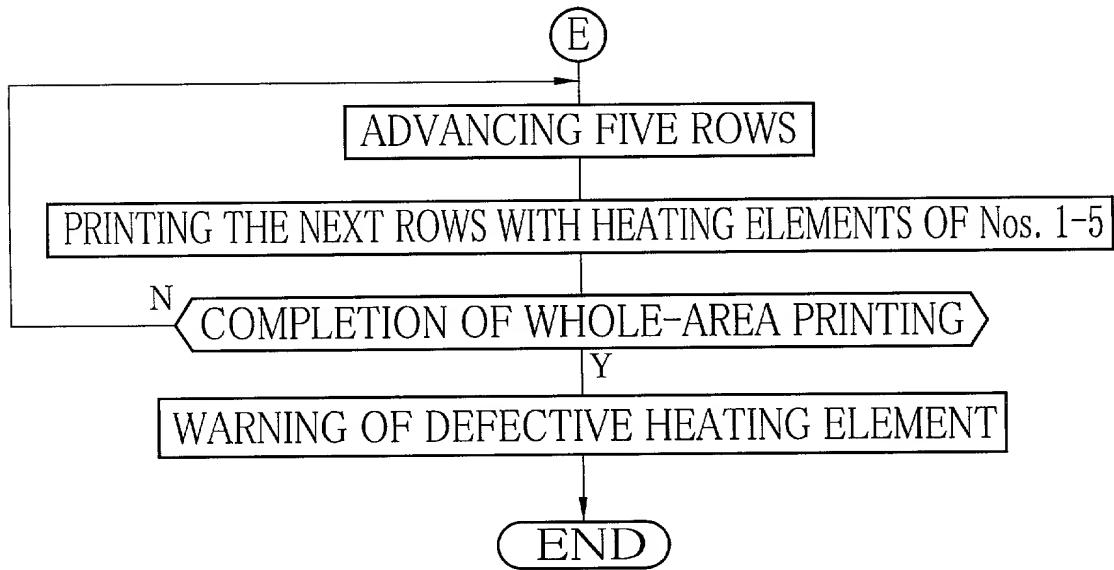
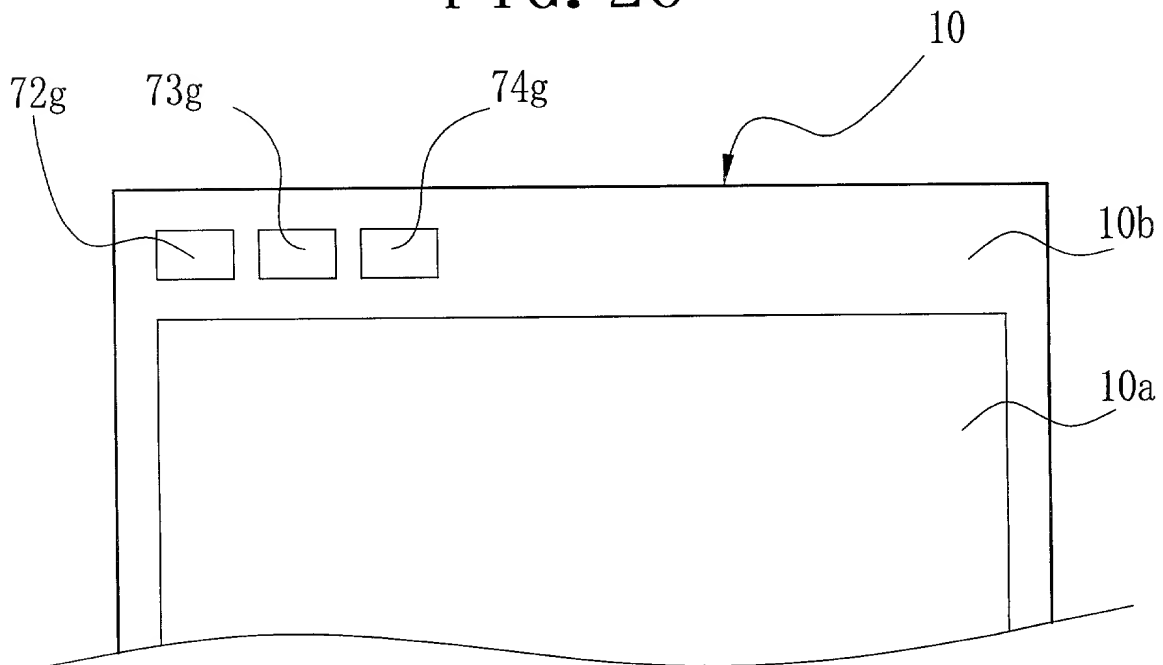


FIG. 26



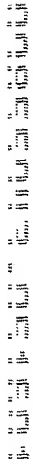
[illegible]

FIG. 22

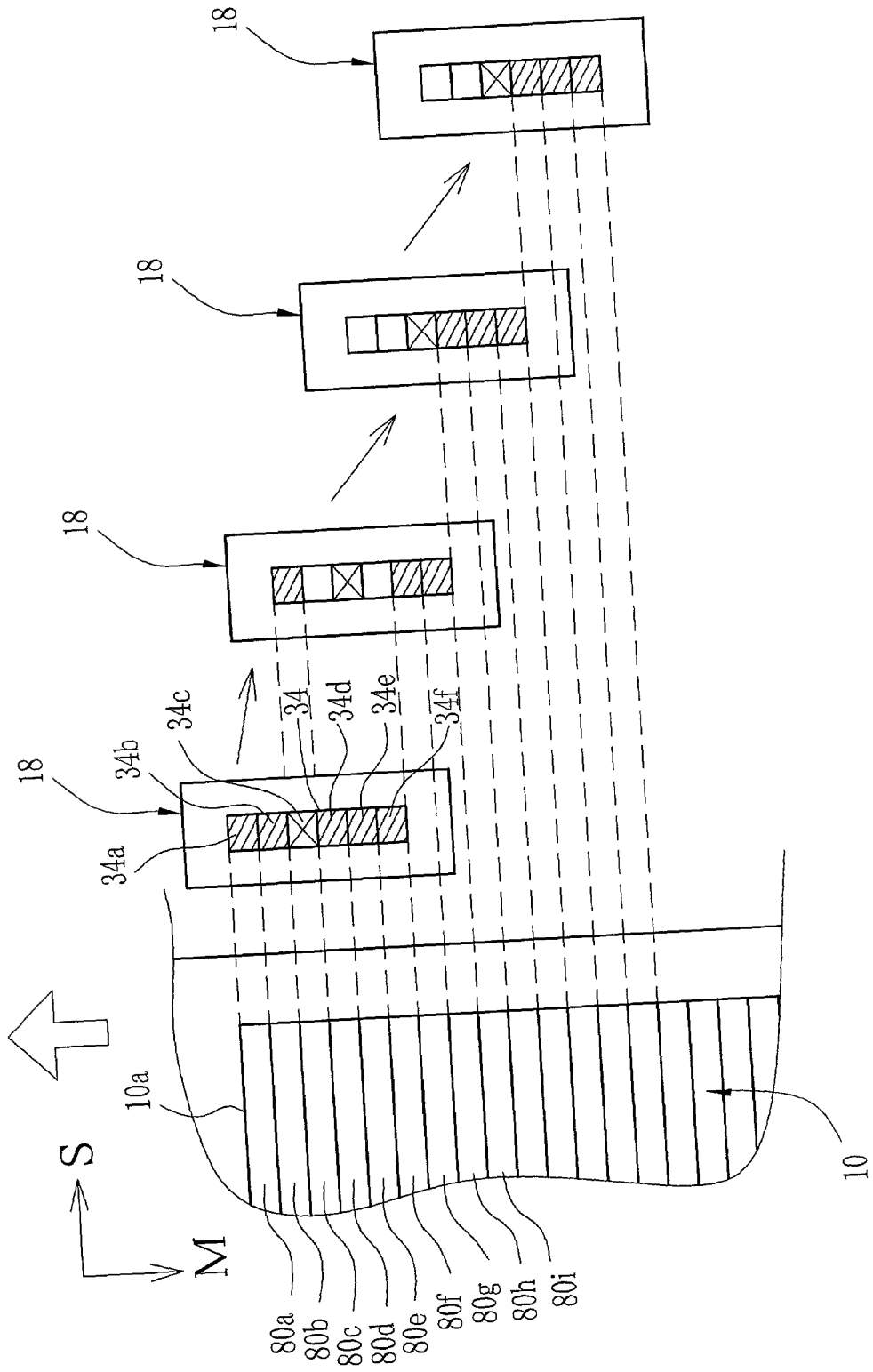


FIG. 23

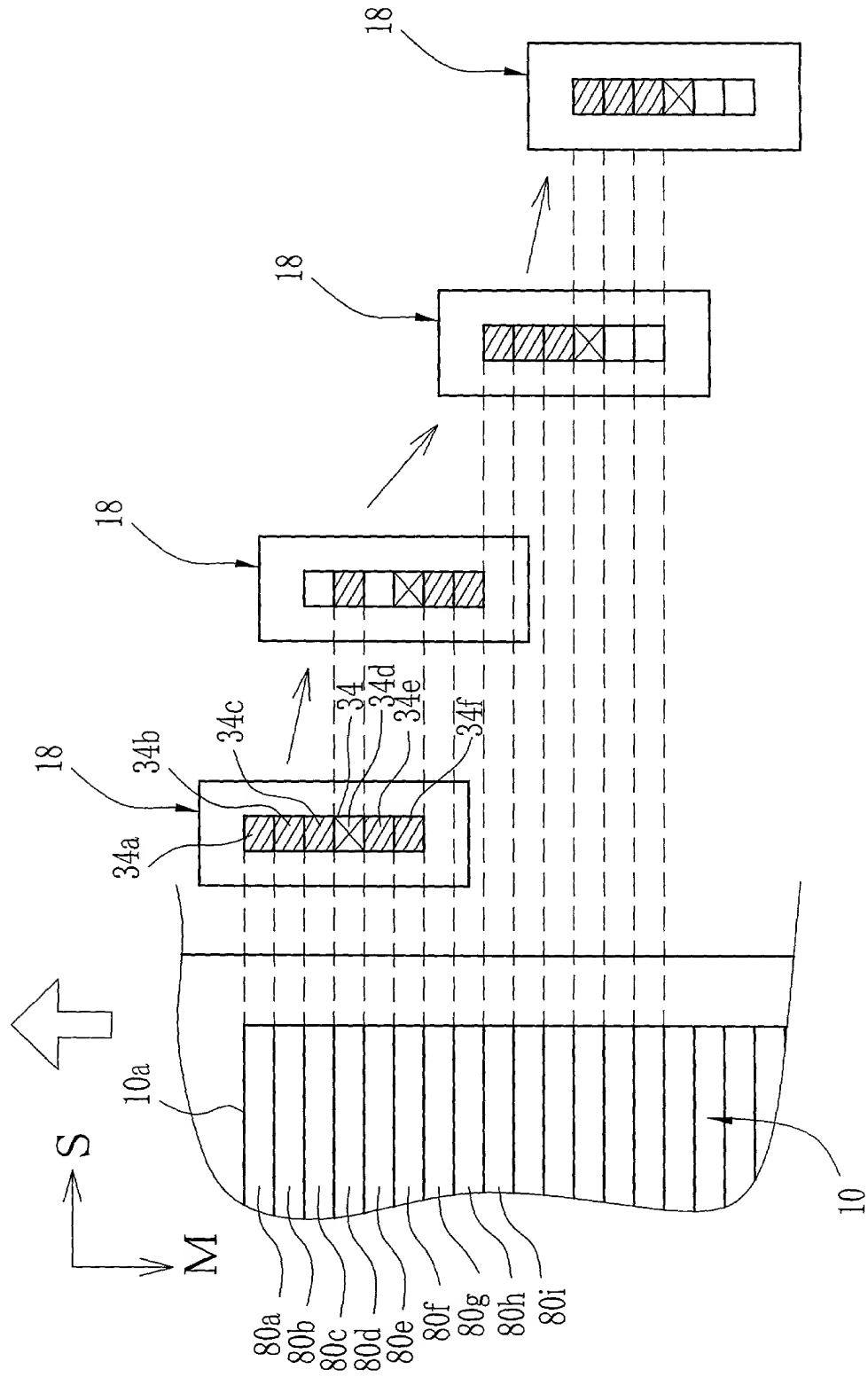


FIG. 24

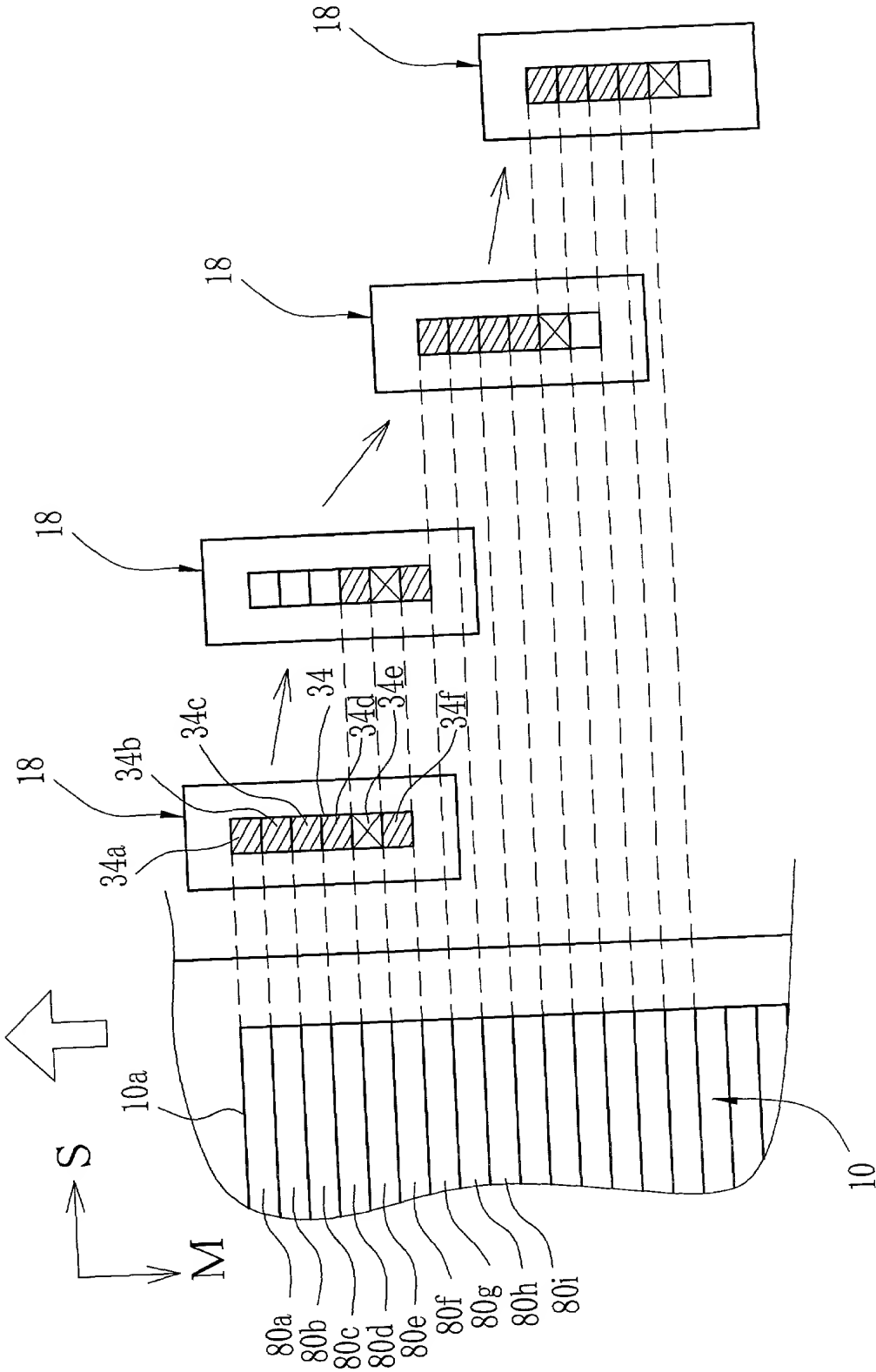


FIG. 27

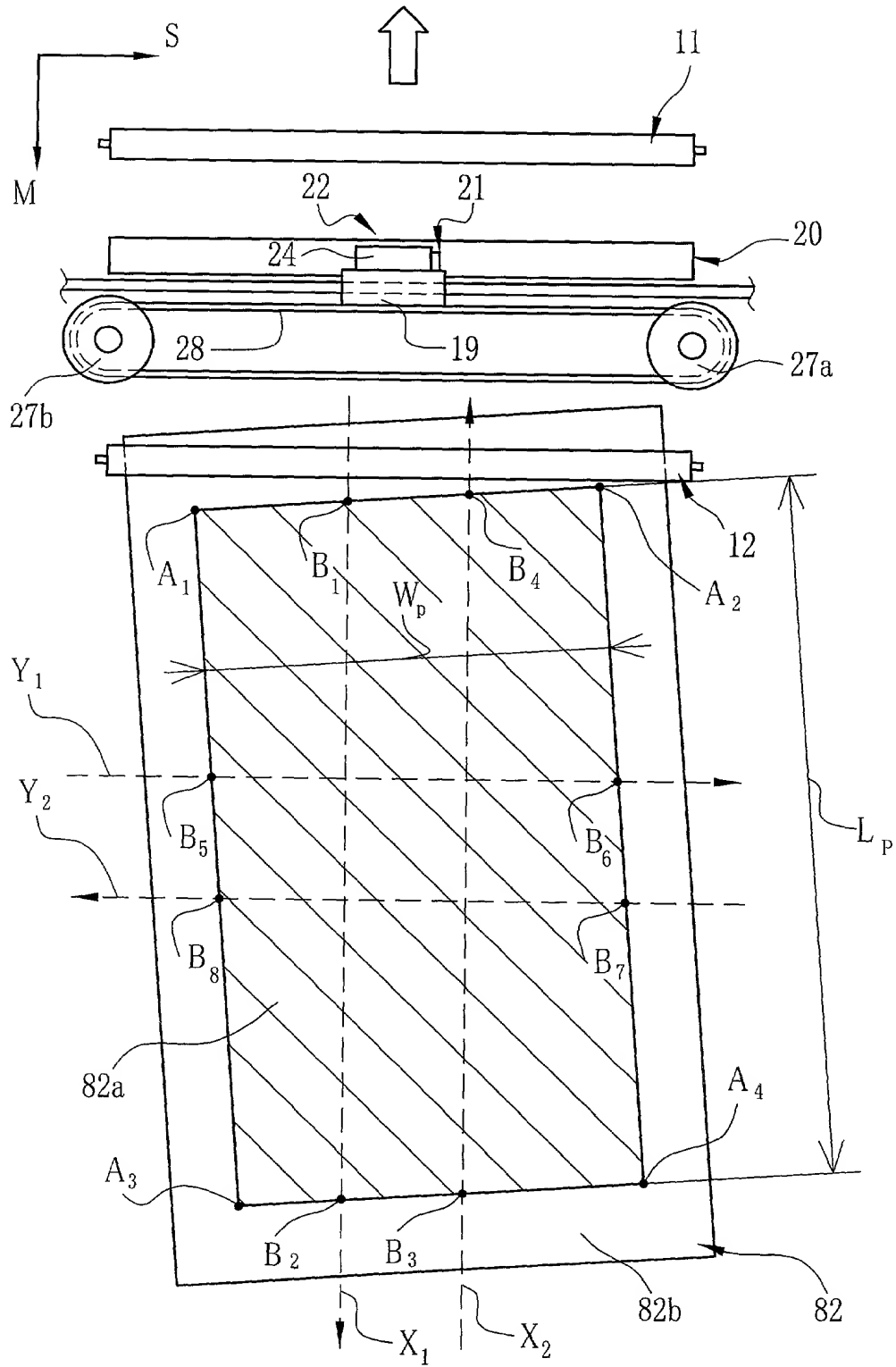


FIG. 28

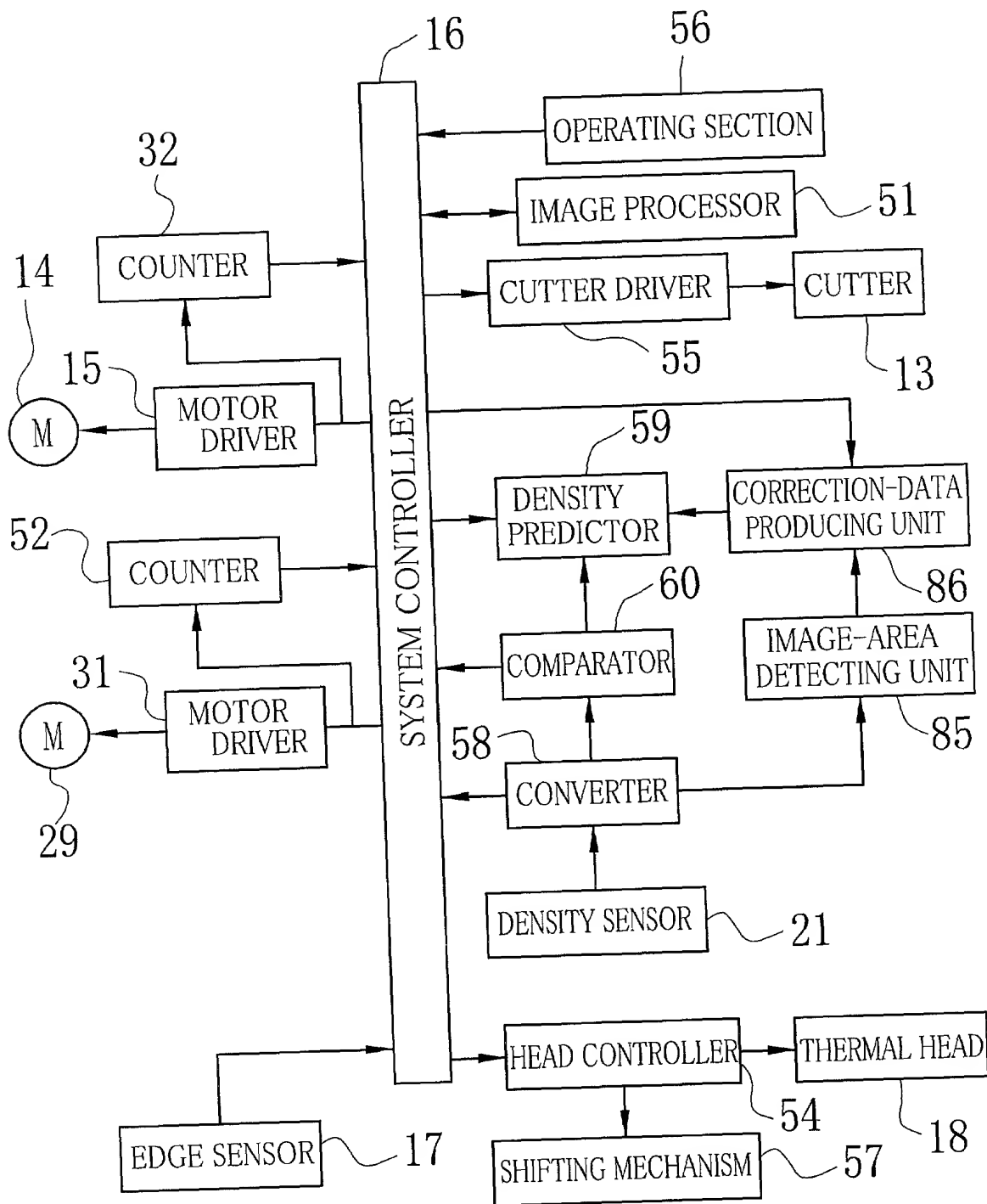


FIG. 29

